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July 23, 2004

Atty Docket No. :	JCLA5312
Appl. No. :	09/853,005
Filing Date :	May, 09, 2001
Pages :	Cover + 8

BY FACSIMILE ONLY

Fax No. :	703-872-9306
Attention :	EXAMINER : BAKER, PAUL A.
Group Unit :	2187
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith are: [x] Transmittal sheet [x] Amendment in 7 pages.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on July 23, 2004 at the above indicated fax number.

Sign by:


Michelle Chang

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Application No.:09/853,005

Docket No.: JCLA5312

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re PATENT APPLICATION of
Applicants : TSAI ET AL.

Serial No. : 09/853,005

Examiner : BAKER, PAUL A

Filed : May 09, 2001

Group Art Unit : 2188

For : PERIPHERAL DEVICE INTERFACE
CHIP CACHE AND DATA
SYNCHRONIZATION METHOD

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AMENDMENT AND RESPONSE TO OFFICE ACTIONMail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Office Action mailed dated March 24, 2004, the Applicant respectfully submits
the following Amendments and Remarks.